

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTORNEY DOCKET NO. 1514.1030	APPLICATION NO. 10/734,162
LIST OF REFERENCES CITED BY APPLICANT  (Use several sheets if necessary)		FIRST NAMED INVENTOR Ji Yong PARK et al.	
		FILING DATE December 15, 2003	GROUP PART UNIT 2815

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	TRANSLATION YES NO		ABSTRACT
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

				TRANSLATION YES NO	
	AM	Park, Ji-Yong, et al. "Characteristics of Thin Film Transistors Fabricated Employing Various Sequential Lateral Solidification Poly-Si Microstructures". Vo. 19, No. 2. February 2004. Materials Research Society.		X	

EXAMINER	DATE CONSIDERED
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>	